ABSTRACT

Provided is a repair fuse box of a semiconductor device comprising: a plurality of fuse boxes arranged in a longitudinal direction, each fuse box comprising a plurality of fuses arranged in a transverse direction; signal connecting fuses arranged in each side portion of outermost fuses of the fuses which are selected from the fuses arranged in one or more fuse boxes of the plurality of fuse boxes to construct a unit fuse set and of which one side ends are connected mutually, the signal connecting fuses being connected to the outermost fuses; and metal lines for connecting the signal connecting fuses to the fuses selected to construct a unit fuse box in an upper or lower fuse box. According to the present invention, it is possible to connect fuses together without detour of the metal lines and to considerably reduce a chip size.

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